Host Processor

UniFinger Module

UART

H_RX

H_TX

1
2
(1)
3

74HC08

H_TXD0

H_RXD0

(2)

I/O

POWER_ENABLE

V_IN_5V

1 SD#   ERROR# 5

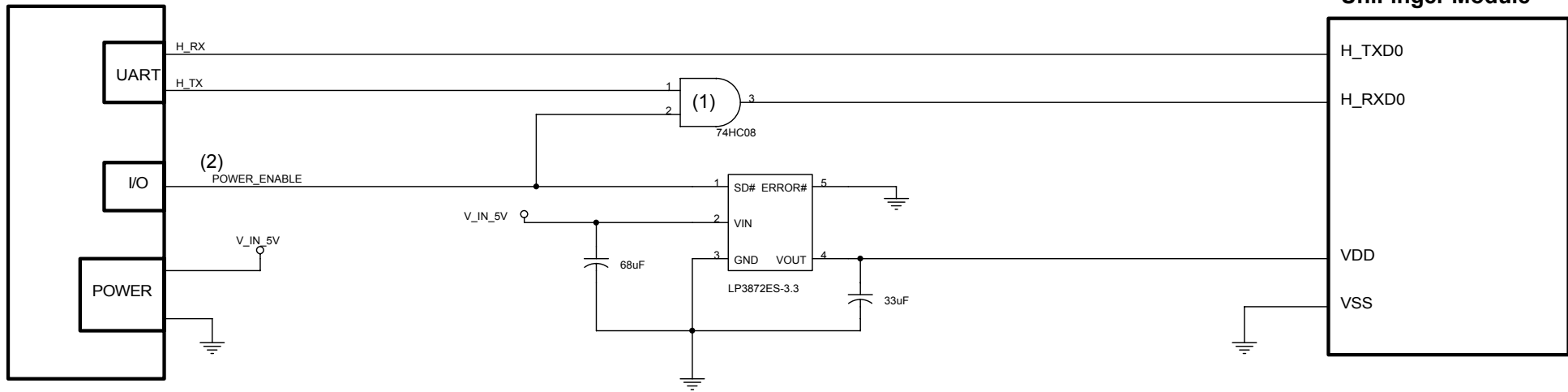2 VIN

3 GND   VOUT 4

LP3872ES-3.3

68uF

33uF

V_IN_5V

POWER

VDD

VSS

Notes
(1) MAY BE OMITTED IF H_TX CAN BE TRI-STATED OR PULLED LOW WHILE THE UNIFINGER MODULE IS POWERED DOWN
(2) POWER_ENABLE: LOW -> POWER OFF, HIGH -> POWER ON

* CAUTION
All I/O and signal pins connected to the UniFinger module should be in 0 ~ 0.3V range while the module
is powered down, to avoid undesired side effects and/or permanent damage.

Title
UniFinger Host I/F with power control circuit

Size	Document Number	Rev
A4		0.9

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